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Mickael Gros-Jean received his PhD degree in science in 1999 from the Joseph Fourier University (Grenoble, France).

He joined STMicroelectronics in 2001, where he started high-k dielectrics deposition developments for MIM capacitors. In 2005 he introduced PEALD for capacitors applications, with a focus on both materials properties and tools productivity. He also extended his work to embedded DRAM and High K/Metal Gate Films.

Since 2012 he leads High-k and epitaxy developments at the STMicroelectronics R&D center of Crolles (France).